SN5400, SN54LS00, SN54S00 SN7400, SN74LS00, SN74S00 QUADRUPLE 2-INPUT POSITIVE-NAND GATES SDLS025 – DECEMBER 1983 – REVISED MARCH 1988

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

description

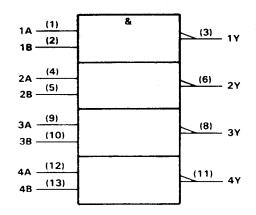
These devices contain four independent 2-input-NAND gates.

The SN5400, SN54LS00, and SN54S00 are characterized for operation over the full military temperature range of -55 °C to 125 °C. The SN7400, SN74LS00, and SN74S00 are characterized for operation from 0 °C to 70 °C.

FUNCTION TABLE (each gate)

INP	UTS	OUTPUT
A	B	Y
н	н	L
L	х	н
x	L	н

logic symbol[†]



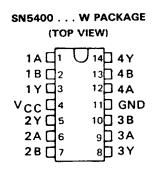
 $^{\dagger}\mbox{This symbol is in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12.$

Pin numbers shown are for D, J, and N packages.

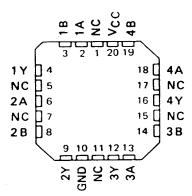
SN5400 . . . J PACKAGE SN54LS00, SN54S00 . . . J OR W PACKAGE SN7400 . . . N PACKAGE SN74LS00, SN74S00 . . . D OR N PACKAGE

(TOP VIEW)

1A [18 [1Y [1 2 3	U 14 VCC 13 4 B 12 4A
	1	
1B [2	13] 4 B
] 3	12]] 4A
2A [4	11D 4Y
2B [5	10]] 3 B
2Y [6	9 🗍 3 A
GND [7	8 3 Y

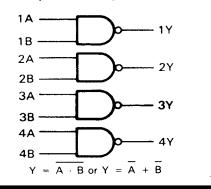


SN54LS00, SN54S00 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

logic diagram (positive logic)



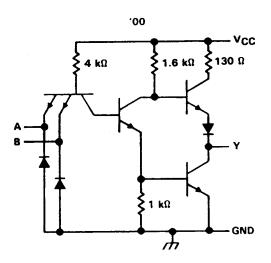
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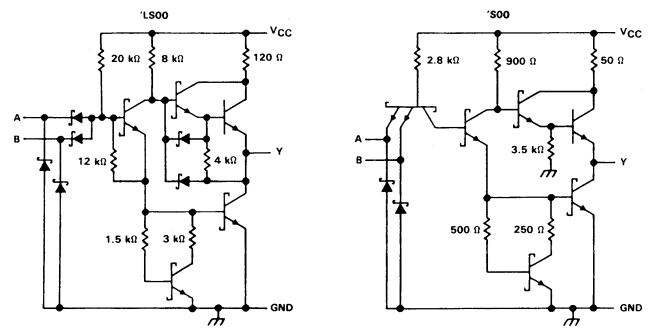
PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



SN5400, SN54LS00, SN54S00 SN7400, SN74LS00, SN74S00 **QUADRUPLE 2-INPUT POSITIVE-NAND GATES** SDLS025 – DECEMBER 1983 – REVISED MARCH 1988

schematics (each gate)





Resistor values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, Vcc (see Note 1)		7 V
Input voltage: '00, 'S00		5.5 V
′LS00		
Operating free-air temperature range:	: SN54'	–55°C to 125°C
	SN74'	0°C to 70°C
Storage temperature range		-65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.



SN5400, SN54LS00, SN54S00 SN7400, SN74LS00, SN74S00 **QUADRUPLE 2-INPUT POSITIVE-NAND GATES**

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recommended operating conditions

	· · · · · · · · · · · · · · · · · · ·		SN5400			UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
v _{cc}	Supply voltage	4.5	5	5.5	4.75	5	5.25	v
VIH	High-level input voltage	2			2			v
VIL	Low-level input voltage			0.8			0.8	v
юн	High-level output current			0.4			- 0.4	mA
IOL	Low-level output current			16			16	mA
Τ _Α	Operating free-air temperature	- 55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	TEST CONDITIONS T	SN5400	SN7400	
PARAMETER	TEST CONDITIONS I	MIN TYP‡ MA	K MIN TYP‡ MAX	
VIK	$V_{CC} = MIN, I_I = -12 \text{ mA}$	- 1.	5 – 1.5	V
V _{OH}	V _{CC} = MIN, V _{1L} = 0.8 V, 1 _{OH} = -0.4 mA	2.4 3.4	2.4 3.4	v
VOL	V _{CC} = MIN, V _{IH} = 2 V, I _{OL} = 16 mA	0.2 0.	4 0.2 0.4	v
l į	V _{CC} = MAX, V ₁ = 5.5 V		1 1	mA
Iн	V _{CC} = MAX, V _I = 2.4 V	4	0 40	μA
ΪιL	V _{CC} = MAX, V ₁ = 0.4 V	- 1.	6 – 1.6	mA
IOS §	V _{CC} = MAX	- 20 - 5	5 - 18 - 55	mA
1ссн	V _{CC} = MAX, V _I = 0 V	4	B 4 8	mA
ICCL	V _{CC} = MAX, V ₁ = 4.5 V	12 2	2 12 22	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

[‡] All typical values are at V_{CC} = 5 V, T_A = 25° C. § Not more than one output should be shorted at a time.

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	түр	MAX	UNIT
^t PLH					11	22	ns
^t PHL	A or B	Y	R _L = 400 Ω, C _L = 15 pF		7	15	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



SN5400, SN54LS00, SN54S00 SN7400, SN74LS00, SN74S00 **QUADRUPLE 2-INPUT POSITIVE-NAND GATES**

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recommended operating conditions

			SN54LS00			SN74LS00			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	v	
VIH	High-level input voltage	2			2			V	
VIL	Low-level input voltage			0.7			0.8	V	
юн	High-level output current			- 0.4			- 0.4	mA	
IOL	Low-level output current			4			8	mA	
TA	Operating free-air temperature	55		125	0		70	°C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

				SN54LS00 SN74LS00			00			
PARAMETER		TEST CONDIT	TIONS T	MIN	TYP‡	MAX	MIN	TYP‡	MAX	
VIK	V _{CC} = MIN,	1 ₁ = - 18 mA			_	- 1.5			- 1.5	v
VOH	V _{CC} = MIN,	VIL = MAX,	I _{OH} = - 0.4 mA	2.5	3.4		2.7	3.4		V
N.	V _{CC} = MIN,	V _{IH} = 2 V,	IOL = 4 mA		0.25	0.4		0.25	0.4	- V
VOL	V _{CC} = MIN,	V _{IH} = 2 V,	IOL = 8 mA					0.35	0.5	
ŧ	V _{CC} = MAX,	V = 7 V				0.1			0.1	mA
ін	V _{CC} = MAX,	V ₁ = 2.7 V				20			20	μA
μL	V _{CC} = MAX,	V1 = 0.4 V		·		- 0.4			- 0.4	mA
IOS§	V _{CC} = MAX			- 20		- 100	- 20		- 100	mA
ICCH	V _{CC} = MAX,	V _I = 0 V			0.8	1.6		0.8	1.6	mA
ICCL	V _{CC} = MAX,	V ₁ = 4.5 V			2.4	4.4		2.4	4.4	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

 \ddagger All typical values are at V_{CC} = 5 V, T_A = 25^oC § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

FROM то PARAMETER **TEST CONDITIONS** MIN TYP MAX (INPUT) (OUTPUT) 9 15 ^tPLH Y $R_L = 2 k \Omega$, CL = 15 pF A or B 10 15 ^tPHL

UNIT

ns

ns

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 2)

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



SN5400, SN54LS00, SN54S00 SN7400, SN74LS00, SN74S00 QUADRUPLE 2-INPUT POSITIVE-NAND GATES

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recommended operating conditions

			SN54S00			SN74S00			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	v	
VIH	High-level input voltage	2			2			v	
VIL	Low-level input voltage			0.8			0.8	v	
юн	High-level output current			1			- 1	mA	
IOL	Low-level output current			20			20	mA	
Τ _A	Operating free-air temperature	- 55		125	0		70	°C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	Ī		· · · · · · ·							
PARAMETER		TEST CONDIT	IONST	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT V V mA mA mA mA
VIK	V _{CC} = MIN,	l _l = –18 mA				-1.2			-1.2	v
VOH	V _{CC} = MIN,	V _{IL} = 0.8 V,	^I OH = – 1 mA	2.5	3.4		2.7	3.4		v
VOL	V _{CC} = MIN,	V _{IH} = 2 V,	I _{OL} = 20 mA			0.5			0.5	v
	V _{CC} = MAX,	V _I = 5.5 V				1			1	mA
Чн	V _{CC} = MAX,	V ₁ = 2.7 V				50			50	μA
ЧL	V _{CC} = MAX,	V ₁ = 0.5 V				-2			-2	mA
IOS §	V _{CC} = MAX			-40		-100	-40		-100	mA
ICCH	V _{CC} = MAX,	V1 = 0 V			10	16		10	16	mA
ICCL	V _{CC} = MAX,	V ₁ = 4.5 V			20	36		20	36	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$.

§ Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN TYP	MAX	UNIT
^t PLH			R ₁ = 280 Ω, C _L = 15 pF	3	4.5	ns
^t PHL			$H_{L} = 200.32$, $C_{L} = 10.01$	3	5	ns
^t PLH	A or B			4.5		ns
^t PHL			RL = 280 Ω, CL = 50 pF	5		ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



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